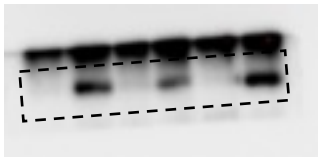


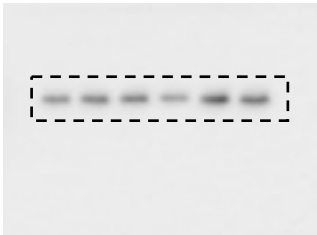
Source Data Fig. 3E



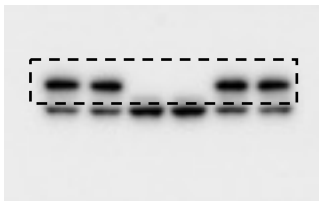
α -FLS2 IP



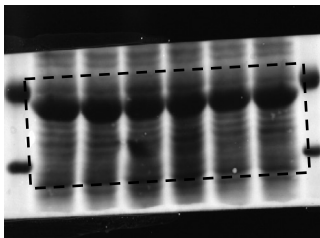
α -BAK1 Co-IP



α -FLS2 input



α -FER input



CBB input



α -BAK1 input